

Notice of References Cited

Application/Control No.

10/525,007

Applicant(s)/Patent Under
Reexamination
ARAI, KAZUHISA

Examiner

M. Wilczewski

Art Unit

2822

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,234,535	08-1993	Beyer et al.	438/459
*	B	US-2005/0009288	01-2005	Cheng et al.	438/407
*	C	US-2003/0219959	11-2003	Ghyselen et al.	438/458
*	D	US-5,429,711	07-1995	Watanabe et al.	438/464
*	E	US-2005/0282472	12-2005	Jeong, In Kwon	451/041
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Partial Machine Translation of JP 2002-75940.
	V	Wolf et al., Silicon Processing for the VLSI Era, Volume 1-Process Technology, Sunset Beach, CA: Lattice Press, 1986, pp. 331-365.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.